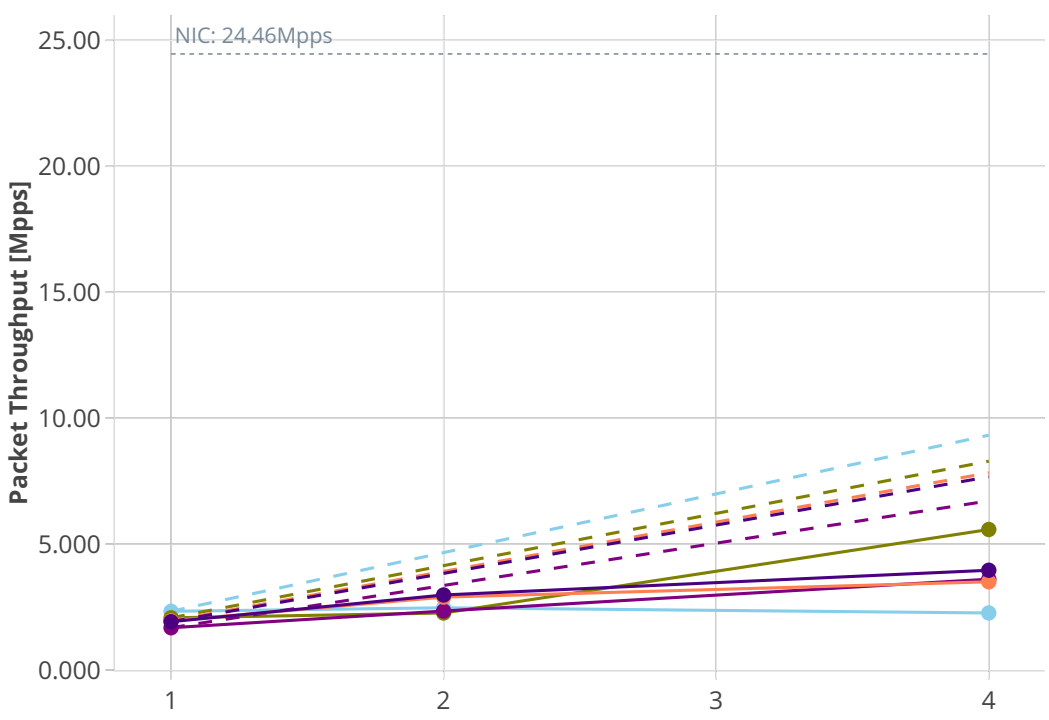


Speedup Multi-core: 3n-tst

3n-tst | x520-640-memif-base-ixgbe-ndr



--- Perfect — Measured ... Limit

- eth-l2xcbase-eth-2memif-1lxc
- eth-l2xcbase-eth-2memif-1dcr
- dot1q-l2bdbasemaclrn-eth-2memif-1dcr
- eth-l2bdbasemaclrn-eth-2memif-1lxc
- ethip4-ip4base-eth-2memif-1dcr